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U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO.

023925-00006

SERIAL NO.

09/855,670

## LIST OF REFERENCES CITED BY APPLICANT

(Use several sheets if necessary)

APPLICANT

Michael A. SOKOL

FILING DATE

May 16, 2001

GROUP

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EXAMINER INITIAL		DOCUMENT NO.	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
llh	AA	5,555,398	Sep. 10, 1996	Raman			Apr. 15, 1994
↑	AB	5,644,784	Jul. 1, 1997	Peek			Mar. 3, 1995
	AD	5,828,653	Oct. 27, 1998	Goss			Apr. 26, 1996
	AE	5,423,015	Jun. 6, 1995	Chung			May 21, 1991
	AF	5,696,899	Dec. 9, 1997	Kalwitz			Nov. 18, 1992
	AG	6,061,351	May 9, 2000	Erimli et al.			Dec. 18, 1997
	AH	5,748,631	May 5, 1998	Bergantino et al.			May 9, 1996
	AI	6,119,196	Sep. 12, 2000	Muller et al.			Jun. 30, 1997
	AJ	6,175,902 B1	Jan. 16, 2001	Runaldue et al.			Dec. 18, 1997
	AK	5,473,607	Dec. 5, 1995	Hausman et al.			Aug. 9, 1993
	AL	5,787,084	Jul. 28, 1998	Hoang et al.			Jun. 5, 1996
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jk	BE 5,918,074	Jun 29, 1999	Wright et al.			Jul. 25, 1997
↑	BF 5,898,687	Apr. 27, 1999	Harriman et al.			Jul. 24, 1996
	BG 5,940,596	Aug. 17, 1999	Rajan et al.			Aug. 4, 1997
	BH 5,802,052	Sep. 1, 1998	Venkataraman			Jun. 26, 1996
	BI 5,842,038	Nov. 24, 1998	Williams et al.			Oct. 10, 1996
	BJ 5,742,613	Apr. 21, 1998	MacDonald			Apr. 27, 1993
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	BR EP0312917A2	Apr. 26, 1989	EPO					
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	BY EP0854606A2	Jul. 22, 1998	EPO					
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	CA WO 99/00949	Jan. 7, 1999	WIPO					
	CB WO 99/00950	Jan. 7, 1999	WIPO					
	CC WO 99/00939	Jan. 7, 1999	WIPO					
	CD WO 99/00938	Jan. 7, 1999	WIPO					
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OTPE	CJ	"Local Area Network Switch Frame Lookup Technique for Increased Speed and Flexibility," 700 IBM Technical Disclosure Bulletin 38(1995) July, No. 7, Armonk, NY, US, pages 221-222
SEP 07 2000	CK	"Queue Management for Shared Buffer and Shared Multi-buffer ATM Switches," Yu-Sheng Lin et al., Department of Electronics Engineering & Institute of Electronics, National Chiao Tung University, Hsinchu, Taiwan, R.O.C., March 24, 1996, pages 688-695
-	CL	"A 622-Mb/s 8 x 8 ATM Switch Chip Set with Shared Multibuffer Architecture," Harufusa Kondoh et al., 8107 IEEE Journal of Solid-State Circuits 28(1993) July, No. 7, New York, US, pages 808-814
✓	CM	"Catalyst 8500 CSR Architecture," White Paper XP-002151999, Cisco Systems Inc. 1998, pages 1-19
1h	CN	"Computer Networks," A.S. Tanenbaum, PRENTICE-HALL INT., USA, XP-002147300(1998), Sec. 5.2-Sec. 5.3, pages 309-320

EXAMINER H. Kim	DATE CONSIDERED 6/7/04
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>	